

KiCad Befehlszeilenschnittstelle

The KiCad Team

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KiCad Nightly Referenzhandbuch

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Alle Markenzeichen in diesem Leitfaden gehören ihren rechtmäßigen Eigentümern.

Mitwirkende

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Feedback

Das KiCad-Projekt freut sich über Rückmeldungen, Fehlerberichte und Vorschläge in Bezug auf die Software oder ihre Dokumentation. Weitere Informationen zum Einreichen von Feedback oder zum Melden eines Problems finden Sie in den Anweisungen unter <https://www.kicad.org/help/report-an-issue/>

Version der Software und Dokumentation

Dieses Benutzerhandbuch basiert auf KiCad 9.99. Funktionalität und Aussehen können sich in anderen Versionen von KiCad unterscheiden.

Revision der Dokumentation: 32e5630e .

Einführung in die KiCad Befehlszeilenschnittstelle

KiCad bietet eine Befehlszeilenschnittstelle, die durch Ausführen der Binärdatei `kicad-cli` verfügbar ist. Mit der Befehlszeilenschnittstelle können Sie eine Reihe von Aktionen für Schaltpläne, Leiterplatten, Symbole und Footprints automatisiert ausführen, z. B. das Plotten von Gerber-Dateien aus einem Leiterplattenentwurf oder das Aktualisieren einer Symbolbibliothek von einem älteren Dateiformat auf ein modernes Format.

NOTE

Unter macOS befindet sich die Anwendung `kicad-cli` unter `/Applications/KiCad/KiCad.app/Contents/MacOS/kicad-cli`.

`kicad-cli` verfügt über 6 Unterbefehle: `fp`, `jobset`, `pcb`, `sch`, `sym` und `version`. Jeder Unterbefehl kann eigene Unterbefehle und Argumente haben. Um beispielsweise Gerber-Dateien aus einer Leiterplatte zu exportieren, könnten Sie `kicad-cli pcb export gerbers example.kicad_pcb` ausführen.

Sie können das Flag `--help` oder `-h` hinzufügen, um Informationen zu den einzelnen Unterbefehlen anzuzeigen. Wenn Sie beispielsweise `kicad-cli pcb -h` ausführen, werden Informationen zur Verwendung des Unterbefehls `pcb` angezeigt, und wenn Sie `kicad-cli pcb export gerbers -h` ausführen, werden Informationen zur Verwendung speziell für den Unterbefehl `pcb export gerbers` angezeigt.

Footprint-Befehle

Der Unterbefehl `fp` exportiert Footprints in ein anderes Format oder aktualisiert die Footprint-Bibliotheken auf die aktuelle Version des KiCad-Footprint-Dateiformats.

Footprint-Export

Der Befehl `fp export svg` exportiert einen oder mehrere Footprints aus der angegebenen Bibliothek in SVG-Dateien.

Usage: `kiCad-cli fp export svg` [--help] [--output OUTPUT_DIR] [--layers LAYER_LIST] [--define-var KEY=VALUE]... [--theme VAR] [--footprint FOOTPRINT_NAME] [--sketch-pads-on-fab-layers] [--hide-DNP-footprints-on-fab-layers] [--sketch-DNP-footprints-on-fab-layers] [--crossout-DNP-footprints-on-fab-layers] [--black-and-white] INPUT_FILE_OR_DIR

Positionsargumente:

INPUT_FILE_OR_DIR	Footprint (<code>.kicad_mod</code>) or footprint library directory (<code>.pretty</code>) to export.
-------------------	--

Optionale Argumente:

<code>-h, --help</code>	Hilfe zum Befehl zum Exportieren von Footprint-SVG-Dateien anzeigen.
<code>-o <Ausgabeverzeichnis>, -output <Ausgabeverzeichnis></code>	Das Ausgabeverzeichnis für die exportierten Dateien. Für jede Ebene jedes Footprints in der Bibliothek wird eine Datei ausgegeben. Wenn <code>--output</code> nicht verwendet wird, werden die Dateien in das aktuelle Verzeichnis exportiert.
<code>-l <Ebenenliste>, --layers <Ebenenliste></code>	Eine durch Kommas getrennte Liste der Ebenennamen, die aus dem Footprint exportiert werden sollen, z. B. <code>F.Cu,B.Cu</code> . Wenn keine Ebenen angegeben sind, werden alle Ebenen exportiert. Layernamen können als kanonische Layernamen (<code>F.Cu, In.1, F.Fab</code> usw.) oder als benutzerdefinierte Layernamen angegeben werden, wobei benutzerdefinierte Layernamen zuerst abgeglichen werden.
<code>-D <Variablenname>=<Wert>, --define-var <Variablenname>=<Wert></code>	Projektvariablendefinitionen hinzufügen oder überschreiben. Kann mehrfach verwendet werden, um mehrere Variablen zu definieren.
<code>-t <Themenname>, --theme <Themenname></code>	Der Name des Themas, das für den Export verwendet werden soll. Wenn kein Thema angegeben ist, wird das derzeit im Footprinteditor ausgewählte Thema verwendet.
<code>--fp <Footprint>, --footprint <Footprint></code>	Der Name des spezifischen Footprints, der aus der Bibliothek exportiert werden soll. Wenn dieses Argument nicht verwendet wird, werden alle Footprints in der Bibliothek exportiert.
<code>--sp, --sketch-pads-on-fab-layers</code>	Pad-Umrisse und deren Nummern auf den vorderen und hinteren Fab-Layern zeichnen.
<code>--hdnp, --hide-DNP-footprints-on-fab-layers</code>	Text und Grafiken von DNP-Footprints nicht auf Fab-Layern plotten.
<code>--sdnp, --sketch-DNP-footprints-on-fab-layers</code>	Grafiken von DNP-Footprints im Skizzenmodus auf Fab-Layern zeichnen.
<code>--cdnp, --crossout-DNP-footprints-on-fab-layers</code>	Ein „X“ über den Hof von DNP-Footprints auf Fab-Layern zeichnen und deren Referenzbezeichnungen durchstreichen.
<code>--black-and-white</code>	Footprints in Schwarz-Weiß exportieren.

Footprint-Upgrade

Der Befehl `fp upgrade` konvertiert die angegebene Footprintbibliothek aus einem älteren KiCad-Footprint-Format oder einem Nicht-KiCad-Footprint-Format in das native Format der aktuellen KiCad-Version. Befindet sich die Eingabebibliothek bereits im aktuellen Dateiformat, wird keine Aktion durchgeführt.

Unterstützte Footprint-Eingabeformate sind:

- KiCad-Footprintbibliothek (Ordner `.pretty` mit `.kicad_mod`-Dateien)
- KiCad (vor 5.0) Footprintbibliothek (`.mod`, `.emp`)
- Altium-Footprintbibliothek (`.PcbLib`)

Altium integrierte Bibliothek (.IntLib)

- CADSTAR PCB-Archiv (.cpa)
- EAGLE XML-Bibliothek (.lbr)
- EasyEDA (JLCEDA) Std-Datei (.json)
- EasyEDA (JLCEDA) Pro-Datei (.elibz, .epro, .zip)
- GEDA/PCB-Bibliothek (Ordner mit .fp-Dateien)

Usage: `kicad-cli fp upgrade [--help] [--output OUTPUT_DIR] [--force] INPUT_FILE_OR_DIR`

Positionsargumente:

INPUT_FILE_OR_DIR	Footprint or footprint library directory to upgrade. For KiCad format footprint libraries, this can be a footprint (.kicad_mod file) or a footprint library (.pretty directory containing .kicad_mod files).
-------------------	--

Optionale Argumente:

-h, --help	Hilfe zum Upgrade-Befehl anzeigen.
-o <Ausgabeverzeichnis>, --output <Ausgabeverzeichnis>	Das Ausgabeverzeichnis für die aktualisierten Footprints. Wenn --output nicht verwendet wird, werden die ursprünglichen Footprints mit den aktualisierten Footprints überschrieben.
--force	Die Eingabebibliothek erneut speichern, auch wenn sie bereits im aktuellen Dateiformat vorliegt.

Jobset-Befehle

Der Befehl `jobset run` führt einen vordefinierten [Jobset](#) aus.

Verwendung: `kicad-cli jobset run [--help] [--stop-on-error] [--file JOB_FILE] [--output OUTPUT] INPUT_FILE`

Positionsargumente:

<code>INPUT_FILE</code>	Projektdatei, die mit dem Jobset verwendet werden soll.
-------------------------	---

Optionale Argumente:

<code>-h, --help</code>	Hilfe für den Befehl „jobset“ anzeigen.
<code>--stop-on-error</code>	Da die Jobs nacheinander ausgeführt werden, wird die Ausführung nach dem Fehlschlagen eines Jobs beendet. Wenn diese Option nicht angegeben wird, werden die Jobs nach dem Fehlschlagen eines Jobs weiter ausgeführt.
<code>-f <Jobset-Datei>, --file <Jobset-Datei></code>	Die auszuführende Jobset-Datei (<code>.kicad_jobset</code>).
<code>--output <Zielbeschreibung oder ID></code>	<p>Das zu generierende Jobset-Ziel. Wenn kein Ziel angegeben ist, werden alle Ziele generiert.</p> <p>Das Ziel wird durch seine Beschreibung oder seine eindeutige ID angegeben. Die angegebene Beschreibung muss eindeutig sein. Wenn der Jobset mehr als ein Ziel mit der angegebenen Beschreibung enthält, wird keines davon ausgeführt.</p> <p>IDs sind von Natur aus eindeutig und können verwendet werden, um auf ein Ziel zu verweisen, auch wenn die Beschreibung des Ziels nicht eindeutig ist. Die ID für jedes Ziel wird vom Befehl <code>jobset run</code> ausgegeben, wenn <code>--output</code> nicht verwendet wird. Sie ist auch in der Datei <code>.kicad_jobset</code> unter dem Schlüssel <code>id</code> des Ziels zu finden.</p>

Platinenkommandos

Der Befehl „pcb“ führt eine Designregelprüfung durch oder exportiert eine Platine in verschiedene andere Dateiformate, darunter Fertigungs- und 3D-Dateien.

PCB DRC

Der Befehl „pcb drc“ führt eine Designregelprüfung auf einer Platine durch und erstellt einen Bericht.

```
Usage: kicad-cli pcb drc [--help] [--output OUTPUT_FILE] [--define-var KEY=VALUE]... [--format
FORMAT] [--all-track-errors] [--schematic-parity] [--units UNITS] [--severity-all] [--severity-
error] [--severity-warning] [--severity-exclusions] [--exit-code-violations] [--refill-zones] [-
-save-board] INPUT_FILE
```

Positionsargumente:

INPUT_FILE	Platinendatei, auf der DRC ausgeführt werden soll.
------------	--

Optionale Argumente:

<code>-h, --help</code>	Show help for the DRC command.
<code>-o <output filename>, --output <output filename></code>	Output filename for the generated DRC report. When <code>--output</code> is not used, the output filename will be the same as the input file, with the <code>.rpt</code> or <code>.json</code> file extension, depending on the selected format.
<code>-D <variable name>=<value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>--format <format></code>	Report file format. Options are <code>report</code> (default) or <code>json</code> .
<code>--all-track-errors</code>	Report all errors for each track.
<code>--schematic-parity</code>	Test for parity between PCB and schematic.
<code>--units <unit></code>	Units to use in the report. Options are <code>mm</code> (default), <code>in</code> , or <code>mils</code> .
<code>--severity-all</code>	Report all DRC violations. This is equivalent to using all of the other DRC severity options.
<code>--severity-error</code>	Report all error-level DRC violations. This can be combined with the other DRC severity options.
<code>--severity-warning</code>	Report all warning-level DRC violations. This can be combined with the other DRC severity options.
<code>--severity-exclusions</code>	Report all excluded DRC violations. This can be combined with the other DRC severity options.
<code>--exit-code-violations</code>	Return an exit code depending on whether or not DRC violations exist. The exit code is 0 if no violations are found, and 5 if any violations are found.
<code>--refill-zones</code>	Refill all zones before running DRC. The board will not be saved after refilling zones unless <code>--save-board</code> is also used.
<code>--save-board</code>	Save the board after running DRC. The board will not be saved unless <code>--refill-zones</code> is also used.

PCB 3D PDF-Export

Der Befehl „`pcb export 3dpdf`“ exportiert ein Leiterplattendesign in eine PDF-Datei, die ein eingebettetes 3D-Modell der Leiterplatte enthält.

Usage: `kicad-cli pcb export 3dpdf [--help] [--output OUTPUT_FILE] [--define-var KEY=VALUE]... [--force] [--no-undefined] [--no-dnp] [--variant VAR]... [--grid-origin] [--drill-origin] [--subst-models] [--board-only] [--cut-vias-in-body] [--no-board-body] [--no-components] [--component-filter VAR] [--include-tracks] [--include-pads] [--include-zones] [--include-inner-copper] [--include-silkscreen] [--include-soldermask] [--fuse-shapes] [--fill-all-vias] [--no-extra-pad-thickness] [--min-distance MIN_DIST] [--net-filter VAR] [--user-origin VAR] INPUT_FILE`

Positionsargumente:

INPUT_FILE	Zu exportierende Platinen-Datei.
------------	----------------------------------

Optionale Argumente:

<code>-h, --help</code>	Show help for the 3D PDF export command.
<code>-o <output filename>, --output <output filename></code>	The output filename. When <code>--output</code> is not used, the output filename will be the same as the input file, with the <code>.pdf</code> file extension.
<code>-D <variable name>=<value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>-f, --force</code>	Overwrite output file.
<code>--no-unspecified</code>	Exclude 3D models of components with "unspecified" footprint type.
<code>--no-dnp</code>	Exclude 3D models of components with "Do not populate" attribute.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>\${VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.
<code>--grid-origin</code>	Use grid origin as origin of output file.
<code>--drill-origin</code>	Use drill origin as origin of output file.
<code>--subst-models</code>	Replace VRML models in footprints with STEP or IGS models of the same name, if they exist.
<code>--board-only</code>	Only include the board itself in the generated model; exclude all component models.
<code>--cut-vias-in-body</code>	Cut via holes in board body even if conductor layers are not exported.
<code>--no-board-body</code>	Exclude board body.
<code>--no-components</code>	Exclude 3D models for components.
<code>--component-filter <reference designator list></code>	Only include component 3D models matching this list of reference designators (comma-separated, wildcards supported)
<code>--include-tracks</code>	Include tracks and vias on outer conductor layers in export (time consuming).
<code>--include-pads</code>	Include pads in export (time consuming).
<code>--include-zones</code>	Include zones in export (time consuming).
<code>--include-inner-copper</code>	Include elements on inner conductor layers in export.
<code>--include-silkscreen</code>	Include silkscreen graphics in export as a set of flat faces.

<code>--include-soldermask</code>	Include solder mask layers in export as a set of flat faces.
<code>--fuse-shapes</code>	Fuse overlapping geometry together in export (time consuming).
<code>--fill-all-vias</code>	Don't cut via holes in conductor layers.
<code>--no-extra-pad-thickness</code>	Disable adding additional metal thickness to pads. When not used, pads have 0.005mm added to their metal thickness, which causes pads to be separate faces in the exported model, distinct from the surrounding metal.
<code>--min-distance <min distance></code>	Tolerance for considering two points to be in the same location. Default: 0.01mm.
<code>--net-filter <net filter></code>	Only include copper items belonging to nets matching this wildcard.
<code>--user-origin <output origin></code>	Specify a custom origin for the output file, with X and Y coordinates. For example, 1x1in, 1x1inch, or 25.4x25.4mm. The default unit is millimeters.

PCB BREP (OCCT) export

Der Befehl „pcb export brep“ exportiert ein Leiterplattendesign in eine BREP-3D-Modelldatei (OCCT-native Boundary Representation).

Usage: `kicad-cli pcb export brep [--help] [--output OUTPUT_FILE] [--define-var KEY=VALUE]... [--force] [--no-unspecified] [--no-dnp] [--variant VAR]... [--grid-origin] [--drill-origin] [--subst-models] [--board-only] [--cut-vias-in-body] [--no-board-body] [--no-components] [--component-filter VAR] [--include-tracks] [--include-pads] [--include-zones] [--include-inner-copper] [--include-silkscreen] [--include-soldermask] [--fuse-shapes] [--fill-all-vias] [--no-extra-pad-thickness] [--min-distance MIN_DIST] [--net-filter VAR] [--user-origin VAR] INPUT_FILE`

Positionsargumente:

<code>INPUT_FILE</code>	Zu exportierende Platinen-Datei.
-------------------------	----------------------------------

Optionale Argumente:

<code>-h, --help</code>	Show help for the BREP export command.
<code>-o <output filename>, --output <output filename></code>	The output filename. When <code>--output</code> is not used, the output filename will be the same as the input file, with the <code>.brep</code> file extension.

<code>-D <variable name>= <value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>-f, --force</code>	Overwrite output file.
<code>--no-unspecified</code>	Exclude 3D models of components with "unspecified" footprint type.
<code>--no-dnp</code>	Exclude 3D models of components with "Do not populate" attribute.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>#{VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.
<code>--grid-origin</code>	Use grid origin as origin of output file.
<code>--drill-origin</code>	Use drill origin as origin of output file.
<code>--subst-models</code>	Replace VRML models in footprints with STEP or IGS models of the same name, if they exist.
<code>--board-only</code>	Only include the board itself in the generated model; exclude all component models.
<code>--cut-vias-in-body</code>	Cut via holes in board body even if conductor layers are not exported.
<code>--no-board-body</code>	Exclude board body.
<code>--no-components</code>	Exclude 3D models for components.
<code>--component-filter <reference designator list></code>	Only include component 3D models matching this list of reference designators (comma-separated, wildcards supported)
<code>--include-tracks</code>	Include tracks and vias on outer conductor layers in export (time consuming).
<code>--include-pads</code>	Include pads in export (time consuming).
<code>--include-zones</code>	Include zones in export (time consuming).
<code>--include-inner-copper</code>	Include elements on inner conductor layers in export.
<code>--include-silkscreen</code>	Include silkscreen graphics in export as a set of flat faces.
<code>--include-soldermask</code>	Include solder mask layers in export as a set of flat faces.
<code>--fuse-shapes</code>	Fuse overlapping geometry together in export (time consuming).

<code>--fill-all-vias</code>	Don't cut via holes in conductor layers.
<code>--no-extra-pad-thickness</code>	Disable adding additional metal thickness to pads. When not used, pads have 0.005mm added to their metal thickness, which causes pads to be separate faces in the exported model, distinct from the surrounding metal.
<code>--min-distance <min distance></code>	Tolerance for considering two points to be in the same location. Default: 0.01mm.
<code>--net-filter <net filter></code>	Only include copper items belonging to nets matching this wildcard.
<code>--user-origin <output origin></code>	Specify a custom origin for the output file, with X and Y coordinates. For example, 1x1in, 1x1inch, or 25.4x25.4mm. The default unit is millimeters.

PCB Bohrdatei Export

Der Befehl „pcb export drill“ exportiert eine Bohrdatei aus einer Platine.

```
Usage: kicad-cli pcb export drill [--help] [--output OUTPUT_DIR] [--format FORMAT] [--drill-origin DRILL_ORIGIN] [--excellon-zeros-format ZEROS_FORMAT] [--excellon-oval-format OVAL_FORMAT] [--excellon-units UNITS] [--excellon-mirror-y] [--excellon-min-header] [--excellon-separate-th] [--generate-map] [--generate-report] [--report-path REPORT_FILE] [--generate-tenting] [--map-format MAP_FORMAT] [--gerber-precision VAR] INPUT_FILE
```

Positionsargumente:

INPUT_FILE	Zu exportierende Platinen-Datei.
------------	----------------------------------

Optionale Argumente:

<code>-h, --help</code>	Hilfe für den Befehl zum Exportieren von Bohrdateien anzeigen.
<code>-o <Ausgabeverzeichnis>, --output <Ausgabeverzeichnis></code>	Das Ausgabeverzeichnis für die Bohrdatei(en). Wenn <code>--output</code> nicht verwendet wird, werden die Bohrdatei(en) im aktuellen Verzeichnis gespeichert.
<code>--format <Format></code>	Das Bohrdateiformat. Optionen sind <code>excellon</code> (Standard) oder <code>gerber</code> .
<code>--drill-origin <Ursprung></code>	Der Koordinatenursprung für die Bohrdatei. Optionen sind <code>absolute</code> (Standard) zur Verwendung des absoluten Ursprungs der Platine oder <code>plot</code> zur Verwendung des Bohr-/Platzierungsursprungs der Platine.
<code>--excellon-zeros-format <Format></code>	Das Nullformat für die Bohrdatei. Optionen sind <code>decimal</code> (Standard), <code>suppressleading</code> , <code>suppresstrailing</code> oder <code>keep</code> . Gilt nur für Bohrdateien im Excellon-Format.
<code>--excellon-oval-format <Format></code>	Den Bohrmodus für ovale Löcher steuern. Optionen sind <code>route</code> und <code>alternate</code> (Standard). Gilt nur für Bohrdateien im Excellon-Format.
<code>-u <Einheit>, --excellon-units <Einheit></code>	Die Einheit für die Bohrdatei. Optionen sind <code>mm</code> (Standard) oder <code>in</code> . Gilt nur für Bohrdateien im Excellon-Format.
<code>--excellon-mirror-y</code>	Die Bohrdatei in Y-Richtung spiegeln. Gilt nur für Bohrdateien im Excellon-Format.
<code>--excellon-min-header</code>	Einen minimalen Header in der Bohrdatei verwenden. Gilt nur für Bohrdateien im Excellon-Format.
<code>--excellon-separate-th</code>	Separate Bohrdateien für durchkontaktierte und nicht durchkontaktierte Bohrungen erstellen. Gilt nur für Bohrdateien im Excellon-Format.
<code>--generate-map</code>	Zusätzlich zur Bohrdatei eine Kartendatei erstellen.
<code>--generate-report</code>	Eine Berichtdatei mit allen Bohrpunkten erstellen.
<code>--report-path <report filename></code>	Der Ausgabedateiname für die Bohrberichtdatei. Wenn <code>--report-path</code> nicht verwendet wird, entspricht der Dateiname des Berichts dem der Eingabedatei, mit dem Suffix und der Dateiendung <code>-drill.rpt</code> .
<code>--generate-tenting</code>	Separate Bohrdateien für überzeltete Bohrungen generieren. Gilt nur für Bohrdateien im Gerber X2-Format.
<code>--map-format <Format></code>	Das Format der Map-Datei. Optionen sind <code>pdf</code> (Standard), <code>gerberx2</code> , <code>ps</code> , <code>dxf</code> oder <code>svg</code> .
<code>--gerber-precision <precision></code>	Die Genauigkeit (Anzahl der Stellen) für die Bohrdatei. Gültige Optionen sind <code>5</code> oder <code>6</code> (Standard). Gilt nur für Bohrdateien im Gerber-Format.

PCB DXF-Export

Der Befehl „pcb export dxf“ exportiert ein Leiterplattendesign in eine DXF-Datei.

Usage: kicad-cli pcb export dxf [--help] [--output OUTPUT_DIR] [--layers LAYER_LIST] [--common-layers COMMON_LAYER_LIST] [--drawing-sheet SHEET_PATH] [--define-var KEY=VALUE]... [--exclude-refdes] [--exclude-value] [--sketch-pads-on-fab-layers] [--hide-DNP-footprints-on-fab-layers] [--sketch-DNP-footprints-on-fab-layers] [--crossout-DNP-footprints-on-fab-layers] [--subtract-soldermask] [--use-contours] [--use-drill-origin] [--include-border-title] [--output-units UNITS] [--drill-shape-opt VAR] [--mode-single] [--mode-multi] [--scale SCALE] [--check-zones] [--variant VAR]... INPUT_FILE

Position arguments:

INPUT_FILE	Zu exportierende Platinen-Datei.
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Optionale Argumente:

-h, --help	Show help for the DXF export command.
-o <output dir>, --output <output dir>	The output folder or filename for the exported files. When --mode-single is used, this is the output filename. If --output is not used, the output filename will be the same as the input file, with the .pdf file extension. When --mode-multi is used, this is the output directory. If --output is not used, the files are exported to the current directory.
-l <layer list>, --layers <layer list>	A comma-separated list of layer names to export from the footprint, such as F.Cu,B.Cu. At least one layer must be given. Layer names can be specified as canonical layer names (F.Cu, In.1, F.Fab, etc.) or as user-defined (custom) layer names, but user-defined layer names are matched first.
--cl <layer list>, --common-layers <layer list>	A comma-separated list of layer names to plot on all layers, such as F.Cu,B.Cu. Layer names can be specified as canonical layer names (F.Cu, In.1, F.Fab, etc.) or as user-defined (custom) layer names, but user-defined layer names are matched first.
--drawing-sheet <sheet path>	Path to drawing sheet to use in plot, overriding the drawing sheet specified in the board file.
-D <variable name>=<value>, --define-var <variable_name>=<value>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
--erd, --exclude-refdes	Exclude footprint reference designators from plot.
--ev, --exclude-value	Exclude footprint values from plot.
--sp, --sketch-pads-on-fab-layers	Draw pad outlines and their numbers on front and back fab layers.
--hdnp, --hide-DNP-footprints-on-fab-layers	Don't plot text and graphics of DNP footprints on fab layers.

<code>--sdnp, --sketch-DNP-footprints-on-fab-layers</code>	Plot graphics of DNP footprints in sketch mode on fab layers.
<code>--cdnp, --crossout-DNP-footprints-on-fab-layers</code>	Plot an "X" over the courtyard of DNP footprints on fab layers, and strikeout their reference designators.
<code>--subtract-soldermask</code>	Remove silkscreen from areas without soldermask.
<code>--uc, --use-contours</code>	Plot graphic items using their contours.
<code>--udo, --use-drill-origin</code>	Plot using the drill/place file origin.
<code>--ibt, --include-border-title</code>	Include sheet border and title block in plot.
<code>--ou <unit>, --output-units <unit></code>	Output units. Options are mm or in (default).
<code>--drill-shape-opt <shape></code>	The shape of drill marks in the plot. Options are 0 for no drill marks, 1 for small marks, or 2 for actual size marks (default).
<code>--mode-single</code>	Generates a single file with the output arg path acting as the complete directory and filename path. COMMON_LAYER_LIST does not function in this mode. Instead LAYER_LIST controls all layers plotted.
<code>--mode-multi</code>	Plot the layers to one or more DXF files, with each file representing a single layer from LAYER_LIST. The output path specifies the directory in which the files will be written.
<code>--scale <scale></code>	A scaling factor to use for plotting the PCB. The border and title block are not scaled. A scale factor of 0 autoscales the plot.
<code>--check-zones</code>	Check zone fills and refill zones, if required, prior to export. Any zone fill updates are not saved in the board file.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>#{VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.

PCB GenCAD-Export

Der Befehl `pcb export gencad` exportiert ein Leiterplattendesign in eine GenCAD-Datei.

Usage: `kicad-cli pcb export gencad [--help] [--output OUTPUT_DIR] [--define-var KEY=VALUE]... [--flip-bottom-pads] [--unique-pins] [--unique-footprints] [--use-drill-origin] [--store-origin-coord] INPUT_FILE`

Positionsargumente:

<code>INPUT_FILE</code>	Zu exportierende Platinen-Datei.
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Optionale Argumente:

<code>-h, --help</code>	Hilfe für den DXF-Exportbefehl anzeigen.
<code>-o <Ausgabedateiname>, --output <Ausgabedateiname></code>	Der Ausgabedateiname. Wenn <code>--output</code> nicht verwendet wird, entspricht der Ausgabedateiname dem Namen der Eingabedatei mit der Dateiendung <code>.cad</code> .
<code>-D <Variablenname>=<Wert>, --define-var <Variablenname>=<Wert></code>	Projektvariablen definitionen hinzufügen oder überschreiben. Kann mehrfach verwendet werden, um mehrere Variablen zu definieren.
<code>-f, --flip-bottom-pads</code>	Untere Footprint-Padstacks umdrehen.
<code>--unique-pins</code>	Eindeutige Pin-Namen generieren.
<code>--unique-footprints</code>	Erzeugt eine neue Form für jede Footprint-Instanz (Formate werden nicht wiederverwendet).
<code>--use-drill-origin</code>	Ursprung der Bohr-/Platzierungsdatei als Ursprung verwenden.
<code>--store-origin-coord</code>	Ursprungs koordinaten in der Datei speichern.

PCB Gerber-Export: ein Layer pro Datei

Der Befehl `pcb export gerbers` exportiert ein Leiterplattendesign in Gerber-Dateien, wobei jede Datei einen Layer enthält.

NOTE

Beachten Sie, dass es zwei unterschiedliche Gerber-Exportbefehle gibt: `gerber` und `gerbers`. Der Befehl `gerber` schreibt mehrere PCB-Layer in eine einzige Gerberdatei, während der Befehl `gerbers` mehrere Gerberdateien mit einem PCB-Layer pro Datei schreibt. Der Befehl `gerbers` ist in der Regel der richtige Befehl, um eine Platine herstellen zu lassen.

Usage: `kicad-cli pcb export gerbers [--help] [--output OUTPUT_DIR] [--layers LAYER_LIST] [--common-layers COMMON_LAYER_LIST] [--drawing-sheet SHEET_PATH] [--define-var KEY=VALUE]... [--exclude-refdes] [--exclude-value] [--include-border-title] [--sketch-pads-on-fab-layers] [--hide-DNP-footprints-on-fab-layers] [--sketch-DNP-footprints-on-fab-layers] [--crossout-DNP-footprints-on-fab-layers] [--no-x2] [--no-netlist] [--subtract-soldermask] [--disable-aperture-macros] [--use-drill-file-origin] [--precision PRECISION] [--no-protel-ext] [--check-zones] [--variant VAR]... [--board-plot-params] INPUT_FILE`

Positionsargumente:

INPUT_FILE	Zu exportierende Platinen-Datei.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the Gerber export command.
<code>-o <output dir>, --output <output dir></code>	The output folder for the exported files. One file is output for each layer. When <code>--output</code> is not used, the files are exported to the current directory.
<code>-l <layer list>, --layers <layer list></code>	A comma-separated list of layer names to plot from the board, such as <code>F.Cu,B.Cu</code> . If this argument is not used, all layers will be plotted. A separate output file is plotted for each layer. Layer names can be specified as canonical layer names (<code>F.Cu</code> , <code>In.1</code> , <code>F.Fab</code> , etc.) or as user-defined (custom) layer names, but user-defined layer names are matched first.
<code>--cl <layer list>, --common-layers <layer list></code>	A comma-separated list of layer names to plot on all layers, such as <code>F.Cu,B.Cu</code> . Each layer specified is included in every output file. Layer names can be specified as canonical layer names (<code>F.Cu</code> , <code>In.1</code> , <code>F.Fab</code> , etc.) or as user-defined (custom) layer names, but user-defined layer names are matched first.
<code>--drawing-sheet <sheet path></code>	Path to drawing sheet to use in plot, overriding the drawing sheet specified in the board file.
<code>-D <variable name>=<value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>--erd, --exclude-refdes</code>	Exclude footprint reference designators from plot.
<code>--ev, --exclude-value</code>	Exclude footprint values from plot.
<code>--ibt, --include-border-title</code>	Include the sheet border and title block.
<code>--sp, --sketch-pads-on-fab-layers</code>	Draw pad outlines and their numbers on front and back fab layers.
<code>--hdnp, --hide-DNP-footprints-on-fab-layers</code>	Don't plot text and graphics of DNP footprints on fab layers.
<code>--sdnp, --sketch-DNP-footprints-on-fab-layers</code>	Plot graphics of DNP footprints in sketch mode on fab layers.
<code>--cdnp, --crossout-DNP-footprints-on-fab-layers</code>	Plot an "X" over the courtyard of DNP footprints on fab layers, and strikeout their reference designators.
<code>--no-x2</code>	Do not use the extended X2 format.
<code>--no-netlist</code>	Do not include netlist attributes.

<code>--subtract-soldermask</code>	Remove silkscreen from areas without soldermask.
<code>--disable-aperture-macros</code>	Disable aperture macros.
<code>--use-drill-file-origin</code>	Use drill/place file origin instead of absolute origin.
<code>--precision <precision></code>	The precision (number of digits) for the Gerber files. Valid options are 5 or 6 (default).
<code>--no-protel-ext</code>	Use <code>.gbr</code> file extension instead of Protel file extensions (<code>.gbl</code> , <code>.gtl</code> , etc.).
<code>--check-zones</code>	Check zone fills and refill zones, if required, prior to export. Any zone fill updates are not saved in the board file.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>#{VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.
<code>--board-plot-params</code>	Use the Gerber plot settings already configured in the board file.

PCB GLB-Export

Der Befehl `pcb export glb` exportiert ein Leiterplattendesign in eine GLB-Datei (binäre glTF) für 3D-Modelle.

Usage: `kicad-cli pcb export glb [--help] [--output OUTPUT_FILE] [--define-var KEY=VALUE]... [--force] [--no-unspecified] [--no-dnp] [--variant VAR]... [--grid-origin] [--drill-origin] [--submodels] [--board-only] [--cut-vias-in-body] [--no-board-body] [--no-components] [--component-filter VAR] [--include-tracks] [--include-pads] [--include-zones] [--include-inner-copper] [--include-silkscreen] [--include-soldermask] [--fuse-shapes] [--fill-all-vias] [--no-extra-pad-thickness] [--min-distance MIN_DIST] [--net-filter VAR] [--user-origin VAR] INPUT_FILE`

Positionsargumente:

<code>INPUT_FILE</code>	Zu exportierende Platinen-Datei.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the GLB export command.
<code>-o <output filename>, --output <output filename></code>	The output filename. When <code>--output</code> is not used, the output filename will be the same as the input file, with the <code>.glb</code> file extension.

<code>-D <variable name>= <value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>-f, --force</code>	Overwrite output file.
<code>--no-unspecified</code>	Exclude 3D models of components with "unspecified" footprint type.
<code>--no-dnp</code>	Exclude 3D models of components with "Do not populate" attribute.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>\${VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.
<code>--grid-origin</code>	Use grid origin as origin of output file.
<code>--drill-origin</code>	Use drill origin as origin of output file.
<code>--subst-models</code>	Replace VRML models in footprints with STEP or IGS models of the same name, if they exist.
<code>--board-only</code>	Only include the board itself in the generated model; exclude all component models.
<code>--cut-vias-in-body</code>	Cut via holes in board body even if conductor layers are not exported.
<code>--no-board-body</code>	Exclude board body.
<code>--no-components</code>	Exclude 3D models for components.
<code>--component-filter <reference designator list></code>	Only include component 3D models matching this list of reference designators (comma-separated, wildcards supported)
<code>--include-tracks</code>	Include tracks and vias on outer conductor layers in export (time consuming).
<code>--include-pads</code>	Include pads in export (time consuming).
<code>--include-zones</code>	Include zones in export (time consuming).
<code>--include-inner-copper</code>	Include elements on inner conductor layers in export.
<code>--include-silkscreen</code>	Include silkscreen graphics in export as a set of flat faces.
<code>--include-soldermask</code>	Include solder mask layers in export as a set of flat faces.
<code>--fuse-shapes</code>	Fuse overlapping geometry together in export (time consuming).

<code>--fill-all-vias</code>	Don't cut via holes in conductor layers.
<code>--no-extra-pad-thickness</code>	Disable adding additional metal thickness to pads. When not used, pads have 0.005mm added to their metal thickness, which causes pads to be separate faces in the exported model, distinct from the surrounding metal.
<code>--min-distance <min distance></code>	Tolerance for considering two points to be in the same location. Default: 0.01mm.
<code>--net-filter <net filter></code>	Only include copper items belonging to nets matching this wildcard.
<code>--user-origin <output origin></code>	Specify a custom origin for the output file, with X and Y coordinates. For example, 1x1in, 1x1inch, or 25.4x25.4mm. The default unit is millimeters.

PCB HPGL export

NOTE

`kicad-cli pcb export hpgl` is not functional in KiCad 10.0.

The `pcb export hpgl` command is not functional in KiCad 10.0 as KiCad no longer supports HPGL output. In previous versions of KiCad it exported a board design to an HPGL file. It is included as a non-functional command for compatibility reasons. It will be removed in a future version of KiCad.

Usage: `kicad-cli pcb export hpgl [--help] [--output OUTPUT_DIR] INPUT_FILE`

PCB IPC-2581-Export

Der Befehl `pcb export ipc2581` exportiert ein Leiterplattendesign im IPC-2581-Format.

Usage: `kicad-cli pcb export ipc2581 [--help] [--output OUTPUT_FILE] [--drawing-sheet SHEET_PATH] [--define-var KEY=VALUE]... [--precision PRECISION] [--compress] [--version VAR] [--units VAR] [--bom-col-int-id FIELD_NAME] [--bom-col-mfg-pn FIELD_NAME] [--bom-col-mfg FIELD_NAME] [--bom-col-dist-pn FIELD_NAME] [--bom-col-dist FIELD_NAME] [--variant VAR]... INPUT_FILE`

Positionsargumente:

<code>INPUT_FILE</code>	Zu exportierende Platinen-Datei.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the IPC-2581 export command.
<code>-o <output filename>, --output <output filename></code>	The output filename. When <code>--output</code> is not used, the output filename will be the same as the input file, with the <code>.xml</code> file extension.
<code>--drawing-sheet <sheet path></code>	Path to drawing sheet to use in plot, overriding the drawing sheet specified in the board file.
<code>-D <variable name>=<value>, --define-var <variable name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>--precision <precision></code>	The precision (number of digits after the decimal separator) for the exported file. The default is 6.
<code>--compress</code>	Compress output file as a ZIP file.
<code>--version <IPC-2581 standard version></code>	IPC-2581 standard version to use. Options are <code>B</code> or <code>C</code> (default).
<code>--units</code>	Units to use in export. Options are <code>mm</code> (default) or <code>in</code> .
<code>--bom-col-int-id</code>	Name of the part field to use for the Bill of Materials Internal ID column. This can be any footprint field, or blank to omit this column.
<code>--bom-col-mfg-pn</code>	Name of the part field to use for the Bill of Materials Manufacturer Part Number column. This can be any footprint field, or blank to omit this column.
<code>--bom-col-mfg</code>	Name of the part field to use for the Bill of Materials Manufacturer column. This can be any footprint field, or blank to omit this column.
<code>--bom-col-dist-pn</code>	Name of the part field to use for the Bill of Materials Distributor Part Number column. This can be any footprint field, or blank to omit this column.
<code>--bom-col-dist</code>	Name of the part field to use for the Bill of Materials Distributor column. This can be any footprint field, or blank to omit this column.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>\${VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.

PCB IPC-D-356-Export

Der Befehl `pcb export ipcd356` generiert eine IPC-D-356-Netzliste aus dem Board-Design.

Verwendung: `kiCad-cli pcb export ipcd356 [--help] [--output OUTPUT_FILE] INPUT_FILE`

Positionsargumente:

<code>INPUT_FILE</code>	Zu exportierende Platinen-Datei.
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Optionale Argumente:

<code>-h, --help</code>	Hilfe für den IPC-D-356-Exportbefehl anzeigen.
<code>-o <Ausgabedateiname>, --output <Ausgabedateiname></code>	Der Name der Ausgabedatei. Wenn <code>--output</code> nicht verwendet wird, entspricht der Name der Ausgabedatei dem Namen der Eingabedatei mit der Dateierweiterung <code>.d356</code> .

PCB ODB++ Export

Der Befehl `pcb export odb` exportiert ein Leiterplattendesign im ODB++-Format.

Usage: `kicad-cli pcb export odb [--help] [--output OUTPUT_FILE] [--drawing-sheet SHEET_PATH] [--define-var KEY=VALUE]... [--precision PRECISION] [--compression VAR] [--units VAR] [--variant VAR]... INPUT_FILE`

Positionsargumente:

<code>INPUT_FILE</code>	Zu exportierende Platinen-Datei.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the ODB++ export command.
<code>-o <output filename>, --output <output filename></code>	The output filename, or folder name if no compression is used.
<code>--drawing-sheet <sheet path></code>	Path to drawing sheet to use in plot, overriding the drawing sheet specified in the board file.
<code>-D <variable name>=<value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>--precision <precision></code>	The precision (number of digits after the decimal separator) for the exported file. The default is 2.
<code>--compression <mode></code>	Compression mode. Options are <code>none</code> , <code>zip</code> (default), or <code>tgz</code> .
<code>--units <unit></code>	Units to use in the output file. Options are <code>mm</code> (default) or <code>in</code> .
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>#{VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.

PCB PDF-Export

Der Befehl `pcb export pdf` exportiert ein Leiterplattendesign in eine PDF-Datei. Jede Lage kann als eigene Datei oder als Blatt in einer einzelnen Datei ausgegeben werden.

Usage: kicad-cli pcb export pdf [--help] [--output OUTPUT_DIR] [--layers LAYER_LIST] [--common-layers COMMON_LAYER_LIST] [--drawing-sheet SHEET_PATH] [--define-var KEY=VALUE]... [--mirror] [--exclude-refdes] [--exclude-value] [--include-border-title] [--subtract-soldermask] [--sketch-pads-on-fab-layers] [--hide-DNP-footprints-on-fab-layers] [--sketch-DNP-footprints-on-fab-layers] [--crossout-DNP-footprints-on-fab-layers] [--negative] [--black-and-white] [--theme THEME_NAME] [--drill-shape-opt VAR] [--mode-single] [--mode-separate] [--mode-multipage] [--scale SCALE] [--bg-color COLOR] [--check-zones] [--variant VAR]... INPUT_FILE

Position arguments:

INPUT_FILE	Zu exportierende Platinen-Datei.
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Optionale Argumente:

-h, --help	Show help for the PDF export command.
-o <output dir>, --output <output dir>	The output folder or filename for the exported files. When --mode-single or --mode-multipage is used, this is the output filename. If this argument is not used, the output filename will be the same as the input file, with the .pdf file extension. When --mode-separate is used, this is the output directory. If --output is not used, the files are exported to the current directory.
-l <layer list>, --layers <layer list>	A comma-separated list of layer names to export from the board, such as F.Cu,B.Cu. At least one layer must be given. Layer names can be specified as canonical layer names (F.Cu , In.1 , F.Fab , etc.) or as user-defined (custom) layer names, but user-defined layer names are matched first.
--cl <layer list>, --common-layers <layer list>	A comma-separated list of layer names to plot on all layers, such as F.Cu,B.Cu. Layer names can be specified as canonical layer names (F.Cu , In.1 , F.Fab , etc.) or as user-defined (custom) layer names, but user-defined layer names are matched first.
--drawing-sheet <sheet path>	Path to drawing sheet to use in plot, overriding the drawing sheet specified in the board file.
-D <variable name>=<value>, --define-var <variable_name>=<value>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
-m, --mirror	Mirror the board. This can be useful for showing bottom layers.
--erd, --exclude-refdes	Exclude footprint reference designators from plot.
--ev, --exclude-value	Exclude footprint values from plot.
--ibt, --include-border-title	Include the sheet border and title block.
--subtract-soldermask	Remove silkscreen from areas without soldermask.

<code>--sp, --sketch-pads-on-fab-layers</code>	Draw pad outlines and their numbers on front and back fab layers.
<code>--hdnp, --hide-DNP-footprints-on-fab-layers</code>	Don't plot text and graphics of DNP footprints on fab layers.
<code>--sdnp, --sketch-DNP-footprints-on-fab-layers</code>	Plot graphics of DNP footprints in sketch mode on fab layers.
<code>--cdnp, --crossout-DNP-footprints-on-fab-layers</code>	Plot an "X" over the courtyard of DNP footprints on fab layers, and strikeout their reference designators.
<code>-n, --negative</code>	Plot in negative.
<code>--black-and-white</code>	Plot in black and white.
<code>-t <theme name>, --theme <theme name></code>	The name of the theme to use for export. If no theme is given, the board editor's currently selected theme is used.
<code>--drill-shape-opt</code>	The shape of drill marks in the plot. Options are 0 for no drill marks, 1 for small marks, or 2 for actual size marks (default).
<code>--mode-single</code>	Generates a single file with the output arg path acting as the complete directory and filename path. <code>COMMON_LAYER_LIST</code> does not function in this mode. Instead <code>LAYER_LIST</code> controls all layers plotted. All specified layers are plotted on a single page.
<code>--mode-separate</code>	Plot the layers to one or more PDF files, with each file representing a single layer from <code>LAYER_LIST</code> . The output path specifies the directory in which the files will be written.
<code>--mode-multipage</code>	Plot the layers to a single PDF file with multiple pages, with each page representing a single layer from <code>LAYER_LIST</code> . The output path specifies the complete directory and filename path.
<code>--scale <scale></code>	A scaling factor to use for plotting the PCB. The border and title block are not scaled. A scale factor of 0 autoscales the plot.
<code>--bg-color <color></code>	A background color for the plot. The format can be hex (<code>#rrggbb</code> or <code>#rrggbaa</code>) or CSS (<code>rgb(r,g,b)</code> or <code>rgba(r,g,b,a)</code>).
<code>--check-zones</code>	Check zone fills and refill zones, if required, prior to export. Any zone fill updates are not saved in the board file.

<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>\${VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.
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PCB PLY Datelexport

Der Befehl `pcb export ply` exportiert ein Leiterplattendesign in eine PLY-3D-Modelldatei.

Usage: `kicad-cli pcb export ply [--help] [--output OUTPUT_FILE] [--define-var KEY=VALUE]... [--force] [--no-unspecified] [--no-dnp] [--variant VAR]... [--grid-origin] [--drill-origin] [--subst-models] [--board-only] [--cut-vias-in-body] [--no-board-body] [--no-components] [--component-filter VAR] [--include-tracks] [--include-pads] [--include-zones] [--include-inner-copper] [--include-silkscreen] [--include-soldermask] [--fuse-shapes] [--fill-all-vias] [--no-extra-pad-thickness] [--min-distance MIN_DIST] [--net-filter VAR] [--user-origin VAR] INPUT_FILE`

Positionsargumente:

<code>INPUT_FILE</code>	Zu exportierende Platinen-Datei.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the PLY export command.
<code>-o <output filename>, --output <output filename></code>	The output filename. When <code>--output</code> is not used, the output filename will be the same as the input file, with the <code>.ply</code> file extension.
<code>-D <variable name>=<value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>-f, --force</code>	Overwrite output file.
<code>--no-unspecified</code>	Exclude 3D models of components with "unspecified" footprint type.
<code>--no-dnp</code>	Exclude 3D models of components with "Do not populate" attribute.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>\${VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.
<code>--grid-origin</code>	Use grid origin as origin of output file.
<code>--drill-origin</code>	Use drill origin as origin of output file.
<code>--subst-models</code>	Replace VRML models in footprints with STEP or IGS models of the same name, if they exist.
<code>--board-only</code>	Only include the board itself in the generated model; exclude all component models.

<code>--cut-vias-in-body</code>	Cut via holes in board body even if conductor layers are not exported.
<code>--no-board-body</code>	Exclude board body.
<code>--no-components</code>	Exclude 3D models for components.
<code>--component-filter</code> <reference designator list>	Only include component 3D models matching this list of reference designators (comma-separated, wildcards supported)
<code>--include-tracks</code>	Include tracks and vias on outer conductor layers in export (time consuming).
<code>--include-pads</code>	Include pads in export (time consuming).
<code>--include-zones</code>	Include zones in export (time consuming).
<code>--include-inner-copper</code>	Include elements on inner conductor layers in export.
<code>--include-silkscreen</code>	Include silkscreen graphics in export as a set of flat faces.
<code>--include-soldermask</code>	Include solder mask layers in export as a set of flat faces.
<code>--fuse-shapes</code>	Fuse overlapping geometry together in export (time consuming).
<code>--fill-all-vias</code>	Don't cut via holes in conductor layers.
<code>--no-extra-pad-thickness</code>	Disable adding additional metal thickness to pads. When not used, pads have 0.005mm added to their metal thickness, which causes pads to be separate faces in the exported model, distinct from the surrounding metal.
<code>--min-distance</code> <min distance>	Tolerance for considering two points to be in the same location. Default: 0.01mm.
<code>--net-filter</code> <net filter>	Only include copper items belonging to nets matching this wildcard.
<code>--user-origin</code> <output origin>	Specify a custom origin for the output file, with X and Y coordinates. For example, 1x1in, 1x1inch, or 25.4x25.4mm. The default unit is millimeters.

PCB Positionsdateiexport

Der Befehl `pcb export pos` exportiert eine Positionsdatei aus einem Leiterplattenentwurf.

Usage: `kicad-cli pcb export pos` [`--help`] [`--output` OUTPUT_FILE] [`--side` VAR] [`--format` FORMAT] [`--units` UNITS] [`--bottom-negate-x`] [`--use-drill-file-origin`] [`--smd-only`] [`--exclude-fp-th`] [`--exclude-dnp`] [`--gerber-board-edge`] [`--variant` VAR]... INPUT_FILE

Positionsargumente:

INPUT_FILE	Zu exportierende Platinen-Datei.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the position file export command.
<code>-o <output filename>, --output <output filename></code>	The output filename. When <code>--output</code> is not used, the output filename will be the same as the input file, with the <code>.pos</code> file extension.
<code>--side <side></code>	The side of the board to export. Options are <code>front</code> , <code>back</code> , or <code>both</code> (default). Gerber format does not support <code>both</code> .
<code>--format <format></code>	The position file format. Options are <code>ascii</code> (default), <code>csv</code> , or <code>gerber</code> .
<code>--units <unit></code>	Units for position file. Options are <code>in</code> (default) or <code>mm</code> . This option has no effect for Gerber format.
<code>--bottom-negate-x</code>	Use negative X coordinates for footprints on the bottom layer. This option has no effect for Gerber format.
<code>--use-drill-file-origin</code>	Use drill/place file origin instead of absolute origin. This option has no effect for Gerber format.
<code>--smd-only</code>	Include only surface-mount components. This option has no effect for Gerber format.
<code>--exclude-fp-th</code>	Exclude all footprints with through-hole pads. This option has no effect for Gerber format.
<code>--exclude-dnp</code>	Exclude all footprints with "Do not populate" attribute.
<code>--gerber-board-edge</code>	Include board edge layer in export (Gerber format only).
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>#{VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.

PCB PostScript Export

Der Befehl `pcb export ps` exportiert ein Leiterplattendesign in eine PostScript-Datei.

Usage: `kicad-cli pcb export ps [--help] [--output OUTPUT_DIR] [--layers LAYER_LIST] [--common-layers COMMON_LAYER_LIST] [--drawing-sheet SHEET_PATH] [--define-var KEY=VALUE]... [--mirror] [--exclude-refdes] [--exclude-value] [--include-border-title] [--subtract-soldermask] [--sketch-pads-on-fab-layers] [--hide-DNP-footprints-on-fab-layers] [--sketch-DNP-footprints-on-fab-layers] [--crossout-DNP-footprints-on-fab-layers] [--negative] [--black-and-white] [--theme THEME_NAME] [--drill-shape-opt VAR] [--mode-single] [--mode-multi] [--track-width-correction TRACK_COR] [--x-scale-factor X_SCALE] [--y-scale-factor Y_SCALE] [--force-a4] [--scale SCALE] [--check-zones] [--variant VAR]... INPUT_FILE`

Positionsargumente:

INPUT_FILE	Zu exportierende Platinen-Datei.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the PS export command.
<code>-o <output dir>, --output <output dir></code>	The output folder or filename for the exported files. When <code>--mode-single</code> is used, this is the output filename. If <code>--output</code> is not used, the output filename will be the same as the input file, with the <code>.ps</code> file extension. When <code>--mode-multi</code> is used, this is the output directory. If <code>--output</code> is not used, the files are exported to the current directory.
<code>-l <layer list>, --layers <layer list></code>	A comma-separated list of layer names to export from the board, such as <code>F.Cu,B.Cu</code> . At least one layer must be given. Layer names can be specified as canonical layer names (<code>F.Cu</code> , <code>In.1</code> , <code>F.Fab</code> , etc.) or as user-defined (custom) layer names, but user-defined layer names are matched first.
<code>--cl <layer list>, --common-layers <layer list></code>	A comma-separated list of layer names to plot on all layers, such as <code>F.Cu,B.Cu</code> . Each layer specified is included in every output file. Layer names can be specified as canonical layer names (<code>F.Cu</code> , <code>In.1</code> , <code>F.Fab</code> , etc.) or as user-defined (custom) layer names, but user-defined layer names are matched first.
<code>--drawing-sheet <sheet path></code>	Path to drawing sheet to use in plot, overriding the drawing sheet specified in the board file.
<code>-D <variable name>=<value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>-m, --mirror</code>	Mirror the board. This can be useful for showing bottom layers.
<code>--erd, --exclude-refdes</code>	Exclude footprint reference designators from plot.
<code>--ev, --exclude-value</code>	Exclude footprint values from plot.
<code>--ibt, --include-border-title</code>	Include the sheet border and title block.
<code>--subtract-soldermask</code>	Remove silkscreen from areas without soldermask.
<code>--sp, --sketch-pads-on-fab-layers</code>	Draw pad outlines and their numbers on front and back fab layers.
<code>--hdnp, --hide-DNP-footprints-on-fab-layers</code>	Don't plot text and graphics of DNP footprints on fab layers.
<code>--sdnp, --sketch-DNP-footprints-on-fab-layers</code>	Plot graphics of DNP footprints in sketch mode on fab layers.

<code>--cdnp</code> , <code>--crossout-DNP-footprints-on-fab-layers</code>	Plot an "X" over the courtyard of DNP footprints on fab layers, and strikeout their reference designators.
<code>-n</code> , <code>--negative</code>	Plot in negative.
<code>--black-and-white</code>	Plot in black and white.
<code>-t <theme name></code> , <code>--theme <theme name></code>	The name of the theme to use for export. If no theme is given, the board editor's currently selected theme is used.
<code>--drill-shape-opt</code>	The shape of drill marks in the plot. Options are <code>0</code> for no drill marks, <code>1</code> for small marks, or <code>2</code> for actual size marks (default).
<code>--mode-single</code>	Generates a single file with the output arg path acting as the complete directory and filename path. <code>COMMON_LAYER_LIST</code> does not function in this mode. Instead <code>LAYER_LIST</code> controls all layers plotted.
<code>--mode-multi</code>	Plot the layers to one or more PS files, with each file representing a single layer from <code>LAYER_LIST</code> . The output path specifies the directory in which the files will be written.
<code>-C</code> , <code>--track-width-correction</code>	A global correction, in millimeters, that is added to the size of tracks, vias, and pads when plotted. This correction can be used to correct for errors in the PostScript output device to achieve an exact-scale output.
<code>-X</code> , <code>--x-scale-factor</code>	X scale adjust for exact scale.
<code>-Y</code> , <code>--y-scale-factor</code>	Y scale adjust for exact scale.
<code>-A</code> , <code>--force-a4</code>	Force A4 paper size.
<code>--scale <scale></code>	A scaling factor to use for plotting the PCB. The border and title block are not scaled. A scale factor of <code>0</code> autoscales the plot.
<code>--check-zones</code>	Check zone fills and refill zones, if required, prior to export. Any zone fill updates are not saved in the board file.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>\${VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.

PCB statistics export

The `pcb export stats` command exports a report of statistics about the board design.

Usage: `kiCAD-cli pcb export stats [--help] [--output OUTPUT_FILE] [--format FORMAT] [--units UNITS] [--exclude-footprints-without-pads] [--subtract-holes-from-board] [--subtract-holes-from-copper] INPUT_FILE`

Positionenargumente:

<code>INPUT_FILE</code>	Board file to export statistics from.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the statistics command.
<code>-o <output filename>, --output <output filename></code>	Output filename for the generated statistics report. When <code>--output</code> is not used, the output filename will be the same as the input file, with a <code>_statistics</code> suffix and the <code>.rpt</code> or <code>.json</code> file extension, depending on the selected format.
<code>--format <format></code>	Report file format. Options are <code>report</code> (default) or <code>json</code> .
<code>--units <unit></code>	Units to use in the report. Options are <code>mm</code> (default) or <code>in</code> .
<code>--exclude-footprints-without-pads</code>	Exclude footprints that do not contain any pads from component counts.
<code>--subtract-holes-from-board</code>	Subtract the area of holes from the total board area.
<code>--subtract-holes-from-copper</code>	Subtract the area of holes from the total copper area.

PCB STEP export

The `pcb export step` command exports a board design to a STEP file.

Usage: `kiCAD-cli pcb export step [--help] [--output OUTPUT_FILE] [--define-var KEY=VALUE]... [--force] [--no-unspecified] [--no-dnp] [--variant VAR]... [--grid-origin] [--drill-origin] [--submodels] [--board-only] [--cut-vias-in-body] [--no-board-body] [--no-components] [--component-filter VAR] [--include-tracks] [--include-pads] [--include-zones] [--include-inner-copper] [--include-silkscreen] [--include-soldermask] [--fuse-shapes] [--fill-all-vias] [--no-extra-pad-thickness] [--min-distance MIN_DIST] [--net-filter VAR] [--no-optimize-step] [--user-origin VAR] INPUT_FILE`

Positionenargumente:

<code>INPUT_FILE</code>	Zu exportierende Platinen-Datei.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the STEP file export command.
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<code>-o <output filename>, --output <output filename></code>	The output filename. When <code>--output</code> is not used, the output filename will be the same as the input file, with the <code>.step</code> file extension.
<code>-D <variable name>=<value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>-f, --force</code>	Overwrite output file.
<code>--no-unspecified</code>	Exclude 3D models of components with "unspecified" footprint type.
<code>--no-dnp</code>	Exclude 3D models of components with "Do not populate" attribute.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>{VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.
<code>--grid-origin</code>	Use grid origin as origin of output file.
<code>--drill-origin</code>	Use drill origin as origin of output file.
<code>--subst-models</code>	Replace VRML models in footprints with STEP or IGS models of the same name, if they exist.
<code>--board-only</code>	Only include the board itself in the generated model; exclude all component models.
<code>--cut-vias-in-body</code>	Cut via holes in board body even if conductor layers are not exported.
<code>--no-board-body</code>	Exclude board body.
<code>--no-components</code>	Exclude 3D models for components.
<code>--component-filter <reference designator list></code>	Only include component 3D models matching this list of reference designators (comma-separated, wildcards supported)
<code>--include-tracks</code>	Include tracks and vias on outer conductor layers in export (time consuming).
<code>--include-pads</code>	Include pads in export (time consuming).
<code>--include-zones</code>	Include zones in export (time consuming).
<code>--include-inner-copper</code>	Include elements on inner conductor layers in export.
<code>--include-silkscreen</code>	Include silkscreen graphics in export as a set of flat faces.

<code>--include-soldermask</code>	Include solder mask layers in export as a set of flat faces.
<code>--fuse-shapes</code>	Fuse overlapping geometry together in export (time consuming).
<code>--fill-all-vias</code>	Don't cut via holes in conductor layers.
<code>--no-extra-pad-thickness</code>	Disable adding additional metal thickness to pads. When not used, pads have 0.005mm added to their metal thickness, which causes pads to be separate faces in the exported model, distinct from the surrounding metal.
<code>--min-distance <min distance></code>	Tolerance for considering two points to be in the same location. Default: 0.01mm.
<code>--net-filter <net filter></code>	Only include copper items belonging to nets matching this wildcard.
<code>--no-optimize-step</code>	Do not optimize STEP file. This enables writing parametric curves, which reduces file sizes and write/read times, but may reduce compatibility with other software.
<code>--user-origin <output origin></code>	Specify a custom origin for the output file, with X and Y coordinates. For example, 1x1in, 1x1inch, or 25.4x25.4mm. The default unit is millimeters.

PCB STL export

The `pcb export stl` command exports a board design to an STL 3D model file.

Usage: `kicad-cli pcb export stl [--help] [--output OUTPUT_FILE] [--define-var KEY=VALUE]... [--force] [--no-unspecified] [--no-dnp] [--variant VAR]... [--grid-origin] [--drill-origin] [--submodels] [--board-only] [--cut-vias-in-body] [--no-board-body] [--no-components] [--component-filter VAR] [--include-tracks] [--include-pads] [--include-zones] [--include-inner-copper] [--include-silkscreen] [--include-soldermask] [--fuse-shapes] [--fill-all-vias] [--no-extra-pad-thickness] [--min-distance MIN_DIST] [--net-filter VAR] [--user-origin VAR] INPUT_FILE`

Position arguments:

<code>INPUT_FILE</code>	Zu exportierende Platinen-Datei.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the STL export command.
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<code>-o <output filename>, --output <output filename></code>	The output filename. When <code>--output</code> is not used, the output filename will be the same as the input file, with the <code>.stl</code> file extension.
<code>-D <variable name>=<value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>-f, --force</code>	Overwrite output file.
<code>--no-unspecified</code>	Exclude 3D models of components with "unspecified" footprint type.
<code>--no-dnp</code>	Exclude 3D models of components with "Do not populate" attribute.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>{VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.
<code>--grid-origin</code>	Use grid origin as origin of output file.
<code>--drill-origin</code>	Use drill origin as origin of output file.
<code>--subst-models</code>	Replace VRML models in footprints with STEP or IGS models of the same name, if they exist.
<code>--board-only</code>	Only include the board itself in the generated model; exclude all component models.
<code>--cut-vias-in-body</code>	Cut via holes in board body even if conductor layers are not exported.
<code>--no-board-body</code>	Exclude board body.
<code>--no-components</code>	Exclude 3D models for components.
<code>--component-filter <reference designator list></code>	Only include component 3D models matching this list of reference designators (comma-separated, wildcards supported)
<code>--include-tracks</code>	Include tracks and vias on outer conductor layers in export (time consuming).
<code>--include-pads</code>	Include pads in export (time consuming).
<code>--include-zones</code>	Include zones in export (time consuming).
<code>--include-inner-copper</code>	Include elements on inner conductor layers in export.
<code>--include-silkscreen</code>	Include silkscreen graphics in export as a set of flat faces.

<code>--include-soldermask</code>	Include solder mask layers in export as a set of flat faces.
<code>--fuse-shapes</code>	Fuse overlapping geometry together in export (time consuming).
<code>--fill-all-vias</code>	Don't cut via holes in conductor layers.
<code>--no-extra-pad-thickness</code>	Disable adding additional metal thickness to pads. When not used, pads have 0.005mm added to their metal thickness, which causes pads to be separate faces in the exported model, distinct from the surrounding metal.
<code>--min-distance <min distance></code>	Tolerance for considering two points to be in the same location. Default: 0.01mm.
<code>--net-filter <net filter></code>	Only include copper items belonging to nets matching this wildcard.
<code>--user-origin <output origin></code>	Specify a custom origin for the output file, with X and Y coordinates. For example, 1x1in, 1x1inch, or 25.4x25.4mm. The default unit is millimeters.

PCB STEPZ export

The `pcb export stpz` command exports a board design to a STEPZ (GZIP-compressed STEP) file.

Usage: `kicad-cli pcb export stpz [--help] [--output OUTPUT_FILE] [--define-var KEY=VALUE]... [--force] [--no-unspecified] [--no-dnp] [--variant VAR]... [--grid-origin] [--drill-origin] [--submodels] [--board-only] [--cut-vias-in-body] [--no-board-body] [--no-components] [--component-filter VAR] [--include-tracks] [--include-pads] [--include-zones] [--include-inner-copper] [--include-silkscreen] [--include-soldermask] [--fuse-shapes] [--fill-all-vias] [--no-extra-pad-thickness] [--min-distance MIN_DIST] [--net-filter VAR] [--no-optimize-step] [--user-origin VAR] INPUT_FILE`

Position arguments:

<code>INPUT_FILE</code>	Zu exportierende Platinen-Datei.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the STEPZ file export command.
<code>-o <output filename>, --output <output filename></code>	The output filename. When <code>--output</code> is not used, the output filename will be the same as the input file, with the <code>.stpz</code> file extension.

<code>-D <variable name>= <value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>-f, --force</code>	Overwrite output file.
<code>--no-unspecified</code>	Exclude 3D models of components with "unspecified" footprint type.
<code>--no-dnp</code>	Exclude 3D models of components with "Do not populate" attribute.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>#{VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.
<code>--grid-origin</code>	Use grid origin as origin of output file.
<code>--drill-origin</code>	Use drill origin as origin of output file.
<code>--subst-models</code>	Replace VRML models in footprints with STEP or IGS models of the same name, if they exist.
<code>--board-only</code>	Only include the board itself in the generated model; exclude all component models.
<code>--cut-vias-in-body</code>	Cut via holes in board body even if conductor layers are not exported.
<code>--no-board-body</code>	Exclude board body.
<code>--no-components</code>	Exclude 3D models for components.
<code>--component-filter <reference designator list></code>	Only include component 3D models matching this list of reference designators (comma-separated, wildcards supported)
<code>--include-tracks</code>	Include tracks and vias on outer conductor layers in export (time consuming).
<code>--include-pads</code>	Include pads in export (time consuming).
<code>--include-zones</code>	Include zones in export (time consuming).
<code>--include-inner-copper</code>	Include elements on inner conductor layers in export.
<code>--include-silkscreen</code>	Include silkscreen graphics in export as a set of flat faces.
<code>--include-soldermask</code>	Include solder mask layers in export as a set of flat faces.
<code>--fuse-shapes</code>	Fuse overlapping geometry together in export (time consuming).

<code>--fill-all-vias</code>	Don't cut via holes in conductor layers.
<code>--no-extra-pad-thickness</code>	Disable adding additional metal thickness to pads. When not used, pads have 0.005mm added to their metal thickness, which causes pads to be separate faces in the exported model, distinct from the surrounding metal.
<code>--min-distance <min distance></code>	Tolerance for considering two points to be in the same location. Default: 0.01mm.
<code>--net-filter <net filter></code>	Only include copper items belonging to nets matching this wildcard.
<code>--no-optimize-step</code>	Do not optimize STEP file. This enables writing parametric curves, which reduces file sizes and write/read times, but may reduce compatibility with other software.
<code>--user-origin <output origin></code>	Specify a custom origin for the output file, with X and Y coordinates. For example, 1x1in, 1x1inch, or 25.4x25.4mm. The default unit is millimeters.

PCB U3D export

The `pcb export u3d` command exports a board design to a PDF file containing an embedded 3D model of the board.

Usage: `kicad-cli pcb export u3d [--help] [--output OUTPUT_FILE] [--define-var KEY=VALUE]... [--force] [--no-unspecified] [--no-dnp] [--variant VAR]... [--grid-origin] [--drill-origin] [--subst-models] [--board-only] [--cut-vias-in-body] [--no-board-body] [--no-components] [--component-filter VAR] [--include-tracks] [--include-pads] [--include-zones] [--include-inner-copper] [--include-silkscreen] [--include-soldermask] [--fuse-shapes] [--fill-all-vias] [--no-extra-pad-thickness] [--min-distance MIN_DIST] [--net-filter VAR] [--user-origin VAR] INPUT_FILE`

Position arguments:

<code>INPUT_FILE</code>	Zu exportierende Platinen-Datei.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the 3D PDF export command.
<code>-o <output filename>, --output <output filename></code>	The output filename. When <code>--output</code> is not used, the output filename will be the same as the input file, with the <code>.pdf</code> file extension.

<code>-D <variable name>= <value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>-f, --force</code>	Overwrite output file.
<code>--no-unspecified</code>	Exclude 3D models of components with "unspecified" footprint type.
<code>--no-dnp</code>	Exclude 3D models of components with "Do not populate" attribute.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>#{VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.
<code>--grid-origin</code>	Use grid origin as origin of output file.
<code>--drill-origin</code>	Use drill origin as origin of output file.
<code>--subst-models</code>	Replace VRML models in footprints with STEP or IGS models of the same name, if they exist.
<code>--board-only</code>	Only include the board itself in the generated model; exclude all component models.
<code>--cut-vias-in-body</code>	Cut via holes in board body even if conductor layers are not exported.
<code>--no-board-body</code>	Exclude board body.
<code>--no-components</code>	Exclude 3D models for components.
<code>--component-filter <reference designator list></code>	Only include component 3D models matching this list of reference designators (comma-separated, wildcards supported)
<code>--include-tracks</code>	Include tracks and vias on outer conductor layers in export (time consuming).
<code>--include-pads</code>	Include pads in export (time consuming).
<code>--include-zones</code>	Include zones in export (time consuming).
<code>--include-inner-copper</code>	Include elements on inner conductor layers in export.
<code>--include-silkscreen</code>	Include silkscreen graphics in export as a set of flat faces.
<code>--include-soldermask</code>	Include solder mask layers in export as a set of flat faces.
<code>--fuse-shapes</code>	Fuse overlapping geometry together in export (time consuming).

<code>--fill-all-vias</code>	Don't cut via holes in conductor layers.
<code>--no-extra-pad-thickness</code>	Disable adding additional metal thickness to pads. When not used, pads have 0.005mm added to their metal thickness, which causes pads to be separate faces in the exported model, distinct from the surrounding metal.
<code>--min-distance <min distance></code>	Tolerance for considering two points to be in the same location. Default: 0.01mm.
<code>--net-filter <net filter></code>	Only include copper items belonging to nets matching this wildcard.
<code>--user-origin <output origin></code>	Specify a custom origin for the output file, with X and Y coordinates. For example, 1x1in, 1x1inch, or 25.4x25.4mm. The default unit is millimeters.

PCB SVG export

The `pcb export svg` command exports a board design to an SVG file.

Usage: `kicad-cli pcb export svg [--help] [--output OUTPUT_DIR] [--layers LAYER_LIST] [--common-layers COMMON_LAYER_LIST] [--drawing-sheet SHEET_PATH] [--define-var KEY=VALUE]... [--subtract-soldermask] [--mirror] [--theme THEME_NAME] [--negative] [--black-and-white] [--sketch-pads-on-fab-layers] [--hide-DNP-footprints-on-fab-layers] [--sketch-DNP-footprints-on-fab-layers] [--crossout-DNP-footprints-on-fab-layers] [--page-size-mode MODE] [--fit-page-to-board] [--exclude-drawing-sheet] [--drill-shape-opt SHAPE_OPTION] [--mode-single] [--mode-multi] [--scale SCALE] [--check-zones] [--variant VAR]... INPUT_FILE`

Position arguments:

<code>INPUT_FILE</code>	Zu exportierende Platinen-Datei.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the SVG file export command.
<code>-o <output dir>, --output <output dir></code>	The output folder or filename for the exported files. When <code>--mode-single</code> is used, this is the output filename. If <code>--output</code> is not used, the output filename will be the same as the input file, with the <code>.pdf</code> file extension. When <code>--mode-multi</code> is used, this is the output directory. If <code>--output</code> is not used, the files are exported to the current directory.

<code>-l <layer list>, --layers <layer list></code>	A comma-separated list of layer names to export from the board, such as <code>F.Cu,B.Cu</code> . At least one layer must be given. Layer names can be specified as canonical layer names (<code>F.Cu</code> , <code>In.1</code> , <code>F.Fab</code> , etc.) or as user-defined (custom) layer names, but user-defined layer names are matched first.
<code>--cl <layer list>, --common-layers <layer list></code>	A comma-separated list of layer names to plot on all layers, such as <code>F.Cu,B.Cu</code> . Layer names can be specified as canonical layer names (<code>F.Cu</code> , <code>In.1</code> , <code>F.Fab</code> , etc.) or as user-defined (custom) layer names, but user-defined layer names are matched first.
<code>--drawing-sheet <sheet path></code>	Path to drawing sheet to use in plot, overriding the drawing sheet specified in the board file.
<code>-D <variable name>=<value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>--subtract-soldermask</code>	Remove silkscreen from areas without soldermask.
<code>-m, --mirror</code>	Mirror the board. This can be useful for showing bottom layers.
<code>-t <theme name>, --theme <theme name></code>	The name of the theme to use for export. If no theme is given, the board editor's currently selected theme is used.
<code>-n, --negative</code>	Plot in negative.
<code>--black-and-white</code>	Plot in black and white.
<code>--sp, --sketch-pads-on-fab-layers</code>	Draw pad outlines and their numbers on front and back fab layers.
<code>--hdnp, --hide-DNP-footprints-on-fab-layers</code>	Don't plot text and graphics of DNP footprints on fab layers.
<code>--sdnp, --sketch-DNP-footprints-on-fab-layers</code>	Plot graphics of DNP footprints in sketch mode on fab layers.
<code>--cdnp, --crossout-DNP-footprints-on-fab-layers</code>	Plot an "X" over the courtyard of DNP footprints on fab layers, and strikeouts their reference designators.

<code>--page-size-mode <mode></code>	Set page sizing mode. Options are 0 (default), 1, or 2. 0 sets the output page size to fit the entire sheet, including drawing sheet frame and title block. 1 sets the output page size to match the current page size. 2 sets the output page size to the size of the board itself.
<code>--fit-page-to-board</code>	Set the SVG size to match the board outline. This is equivalent to <code>--page-size-mode 2</code> .
<code>--exclude-drawing-sheet</code>	Plot SVG without a drawing sheet.
<code>--drill-shape-opt</code>	The shape of drill marks in the plot. Options are 0 for no drill marks, 1 for small marks, or 2 for actual size marks (default).
<code>--mode-single</code>	Generates a single file with the output arg path acting as the complete directory and filename path. <code>COMMON_LAYER_LIST</code> does not function in this mode. Instead <code>LAYER_LIST</code> controls all layers plotted.
<code>--mode-multi</code>	Plot the layers to one or more SVG files, with each file representing a single layer from <code>LAYER_LIST</code> . The output path specifies the directory in which the files will be written.
<code>--scale <scale></code>	A scaling factor to use for plotting the PCB. The border and title block are not scaled. A scale factor of 0 autoscales the plot.
<code>--check-zones</code>	Check zone fills and refill zones, if required, prior to export. Any zone fill updates are not saved in the board file.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>#{VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.

PCB VRML export

The `pcb export vrml` command exports a board design to a VRML 3D model file.

Usage: `kicad-cli pcb export vrml [--help] [--output OUTPUT_FILE] [--define-var KEY=VALUE]... [--force] [--no-unspecified] [--no-dnp] [--variant VAR]... [--user-origin VAR] [--units VAR] [--models-dir VAR] [--models-relative] INPUT_FILE`

Position arguments:

<code>INPUT_FILE</code>	Zu exportierende Platinen-Datei.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the VRML export command.
<code>-o <output filename>, --output <output filename></code>	The output filename. When <code>--output</code> is not used, the output filename will be the same as the input file, with the <code>.wr1</code> file extension.
<code>-D <variable name>=<value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>-f, --force</code>	Overwrite output file.
<code>--no-unspecified</code>	Exclude 3D models of components with "unspecified" footprint type.
<code>--no-dnp</code>	Exclude 3D models of components with "Do not populate" attribute.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>#{VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.
<code>--user-origin <output origin></code>	Specify a custom origin for the output file, with X and Y coordinates. For example, <code>1x1in</code> , <code>1x1inch</code> , or <code>25.4x25.4mm</code> . The default unit is millimeters. If this option is not given, the board center is used.
<code>--units <units></code>	Units to use in the output file. Options are <code>mm</code> , <code>m</code> , <code>in</code> (default), or <code>tenths</code> (tenths of an inch).
<code>--models-dir <output model directory></code>	Name of output directory to copy component models into. If not used, component models are embedded into the output file.
<code>--models-relative</code>	With <code>--models-dir</code> , use relative paths in the output file.

PCB XAO export

The `pcb export xao` command exports a board design to an XAO (SALOME/Gmsh) 3D model file.

Usage: `kicad-cli pcb export xao [--help] [--output OUTPUT_FILE] [--define-var KEY=VALUE]... [--force] [--no-unspecified] [--no-dnp] [--variant VAR]... [--grid-origin] [--drill-origin] [--subst-models] [--board-only] [--cut-vias-in-body] [--no-board-body] [--no-components] [--component-filter VAR] [--include-tracks] [--include-pads] [--include-zones] [--include-inner-copper] [--include-silkscreen] [--include-soldermask] [--fuse-shapes] [--fill-all-vias] [--no-extra-pad-thickness] [--min-distance MIN_DIST] [--net-filter VAR] [--user-origin VAR] INPUT_FILE`

Position arguments:

<code>INPUT_FILE</code>	Zu exportierende Platinen-Datei.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the XAO export command.
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<code>-o <output filename>, --output <output filename></code>	The output filename. When <code>--output</code> is not used, the output filename will be the same as the input file, with the <code>.xao</code> file extension.
<code>-D <variable name>=<value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>-f, --force</code>	Overwrite output file.
<code>--no-unspecified</code>	Exclude 3D models of components with "unspecified" footprint type.
<code>--no-dnp</code>	Exclude 3D models of components with "Do not populate" attribute.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>\${VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.
<code>--grid-origin</code>	Use grid origin as origin of output file.
<code>--drill-origin</code>	Use drill origin as origin of output file.
<code>--subst-models</code>	Replace VRML models in footprints with STEP or IGS models of the same name, if they exist.
<code>--board-only</code>	Only include the board itself in the generated model; exclude all component models.
<code>--cut-vias-in-body</code>	Cut via holes in board body even if conductor layers are not exported.
<code>--no-board-body</code>	Exclude board body.
<code>--no-components</code>	Exclude 3D models for components.
<code>--component-filter <reference designator list></code>	Only include component 3D models matching this list of reference designators (comma-separated, wildcards supported)
<code>--include-tracks</code>	Include tracks and vias on outer conductor layers in export (time consuming).
<code>--include-pads</code>	Include pads in export (time consuming).
<code>--include-zones</code>	Include zones in export (time consuming).
<code>--include-inner-copper</code>	Include elements on inner conductor layers in export.
<code>--include-silkscreen</code>	Include silkscreen graphics in export as a set of flat faces.

<code>--include-soldermask</code>	Include solder mask layers in export as a set of flat faces.
<code>--fuse-shapes</code>	Fuse overlapping geometry together in export (time consuming).
<code>--fill-all-vias</code>	Don't cut via holes in conductor layers.
<code>--no-extra-pad-thickness</code>	Disable adding additional metal thickness to pads. When not used, pads have 0.005mm added to their metal thickness, which causes pads to be separate faces in the exported model, distinct from the surrounding metal.
<code>--min-distance <min distance></code>	Tolerance for considering two points to be in the same location. Default: 0.01mm.
<code>--net-filter <net filter></code>	Only include copper items belonging to nets matching this wildcard.
<code>--user-origin <output origin></code>	Specify a custom origin for the output file, with X and Y coordinates. For example, 1x1in, 1x1inch, or 25.4x25.4mm. The default unit is millimeters.

PCB render

The `pcb render` command generates a raytraced rendering of the 3D model of the board and saves it to a PNG or JPEG file.

Usage: `kicad-cli pcb render [--help] [--output OUTPUT_FILE] [--define-var KEY=VALUE]... [--width WIDTH] [--height HEIGHT] [--side SIDE] [--background BG] [--quality QUALITY] [--preset PRESET] [--use-board-stackup-colors VAR] [--floor] [--perspective] [--zoom ZOOM] [--pan VECTOR] [--pivot PIVOT] [--rotate ANGLES] [--light-top COLOR] [--light-bottom COLOR] [--light-side COLOR] [--light-camera COLOR] [--light-side-elevation ANGLE] INPUT_FILE`

Position arguments:

<code>INPUT_FILE</code>	Board file to render.
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Optional Arguments:

<code>-h, --help</code>	Show help for the render command.
<code>-o <output filename>, --output <output filename></code>	The output filename. This argument must be given. The file extension given in this argument determines the output image file format. The filename must end with either <code>.png</code> (for PNG files) or <code>.jpg / .jpeg</code> (for JPG files).

<code>-D <variable name>= <value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>-w <width>, --width <width></code>	Image width in pixels. Default: 1600.
<code>-h <height>, --height <height></code>	Image height in pixels. Default: 900.
<code>--side <side></code>	The side of the board to render. Options are <code>top</code> (default), <code>bottom</code> , <code>left</code> , <code>right</code> , <code>front</code> , or <code>back</code> .
<code>--background <background></code>	Image background. Options are <code>default</code> (default), <code>transparent</code> , or <code>opaque</code> . For PNG files, default is <code>transparent</code> . For JPG files, default is <code>opaque</code> .
<code>--quality <quality></code>	Render quality. Options are <code>basic</code> (default), <code>high</code> , <code>user</code> . When <code>user</code> is specified, the render settings stored in the project are used.
<code>--preset <color preset></code>	Color preset. Options are <code>follow_pcb_editor</code> , <code>follow_plot_settings</code> (default), or <code>legacy_preset_flag</code> .
<code>--use-board-stackup-colors</code>	Colors defined in the board stackup override colors from the preset.
<code>--floor</code>	Enables floor, shadows and post-processing, even if disabled in quality preset.
<code>--perspective</code>	Use perspective projection instead of orthogonal.
<code>--zoom <zoom level></code>	Camera zoom factor as an integer. Default: 1.
<code>--pan <camera pan></code>	Set camera pan location, in millimeters, with the format <code>'X,Y,Z'</code> , e.g. <code>'3,0,0'</code> .
<code>--pivot <pivot></code>	Set pivot point relative to the board center in centimeters, with the format <code>'X,Y,Z'</code> e.g. <code>'-10,2,0'</code> .
<code>--rotate <rotation></code>	Set board rotation around pivot point, in degrees, with the format <code>'X,Y,Z'</code> , e.g. <code>'-45,0,45'</code> for isometric view.
<code>--light-top <intensity></code>	Top light intensity, format <code>'R,G,B'</code> or a single number, range: 0-1.
<code>--light-bottom <intensity></code>	Bottom light intensity, format <code>'R,G,B'</code> or a single number, range: 0-1.
<code>--light-side <intensity></code>	Side lights intensity, format <code>'R,G,B'</code> or a single number, range: 0-1.
<code>--light-camera <intensity></code>	Camera light intensity, format <code>'R,G,B'</code> or a single number, range: 0-1.
<code>--light-side-elevation <elevation></code>	Side lights elevation angle in degrees, range: 0-90.

PCB upgrade

The `pcb upgrade` command converts a KiCad board file from a previous KiCad board file format to the native format for the current version of KiCad. If the input board file is already in the current file format, no action is taken.

Usage: `kiCad-cli pcb upgrade [--help] [--force] INPUT_FILE`

Position arguments:

<code>INPUT_FILE</code>	Board file to upgrade.
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Optional Arguments:

<code>-h, --help</code>	Show help for the upgrade command.
<code>--force</code>	Re-save the input board file even if it is already in the current file format.

Schematic commands

The `sch` command runs an electrical rule check, exports a schematic to various other file formats, or exports a bill of materials or netlist. Each subcommand has its own options.

Schematic ERC

The `sch erc` command runs an electrical rule check on a schematic and generates a report.

Usage: `kicad-cli sch erc [--help] [--output OUTPUT_FILE] [--define-var KEY=VALUE]... [--format VAR] [--units VAR] [--severity-all] [--severity-error] [--severity-warning] [--severity-exclusions] [--exit-code-violations] INPUT_FILE`

Position arguments:

<code>INPUT_FILE</code>	Schematic file to run ERC on.
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Optional Arguments:

<code>-h, --help</code>	Show help for the ERC command.
<code>-o <output filename>, --output <output filename></code>	Output filename for the generated ERC report. When <code>--output</code> is not used, the output filename will be the same as the input file, with the <code>.rpt</code> or <code>.json</code> file extension, depending on the selected format.
<code>-D <variable name>=<value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>--format <format></code>	Report file format. Options are <code>report</code> (default) or <code>json</code> .
<code>--units <unit></code>	Units to use in the report. Options are <code>mm</code> (default), <code>in</code> , or <code>mils</code> .
<code>--severity-all</code>	Report all ERC violations. This is equivalent to using all of the other ERC severity options.
<code>--severity-error</code>	Report all error-level ERC violations. This can be combined with the other ERC severity options.
<code>--severity-warning</code>	Report all warning-level ERC violations. This can be combined with the other ERC severity options.
<code>--severity-exclusions</code>	Report all excluded ERC violations. This can be combined with the other ERC severity options.
<code>--exit-code-violations</code>	Return an exit code depending on whether or not ERC violations exist. The exit code is 0 if no violations are found, and 5 if any violations are found.

Schematic bill of materials export

The `sch export bom` command exports a BOM from a schematic. The BOM export has a number of options for controlling the format and included fields. This export method is equivalent to [exporting a BOM](#) from the symbol fields table.

NOTE

To export a BOM using the legacy XML and Python BOM script workflow, use the `sch export python-bom` command.

Usage: `kiCad-cli sch export bom [--help] [--output OUTPUT_FILE] [--variant VAR]... [--preset PRESET] [--format-preset FMT_PRESET] [--fields FIELDS] [--labels LABELS] [--group-by GROUP_BY] [--sort-field SORT_BY] [--sort-asc VAR] [--filter FILTER] [--exclude-dnp] [--include-excluded-from-bom] [--field-delimiter FIELD_DELIM] [--string-delimiter STR_DELIM] [--ref-delimiter REF_DELIM] [--ref-range-delimiter REF_RANGE_DELIM] [--keep-tabs] [--keep-line-breaks] INPUT_FILE`

Position arguments:

INPUT_FILE	Schematic file to export.
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Optionale Argumente:

<code>-h, --help</code>	Shows help message and exits
<code>-o <output filename>, --output <output filename></code>	The output filename. When <code>--output</code> is not used, the output filename will be the same as the input file, with a <code>.csv</code> file extension.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>#{VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.
<code>--preset <preset></code>	Use a named BOM preset setting from the schematic, e.g. "Grouped By Value".
<code>--format-preset <format preset></code>	Use a named BOM format preset setting from the schematic, e.g. CSV.
<code>--fields <fields></code>	An ordered list of fields to export. <code>*</code> includes all fields. Special symbol fields such as DNP or Exclude from board can be accessed with <code>#{DNP}</code> or <code>#{EXCLUDE_FROM_BOARD}</code> , respectively (see the text variable documentation for a list of fields). Default: "Reference,Value,Footprint,#{QUANTITY},#{DNP}".
<code>--labels <labels></code>	An ordered list of labels to apply the exported fields (default: "Refs, Value, Footprint, Qty, DNP").
<code>--group-by <fields></code>	Fields to group references by when field values match.
<code>--sort-field <fields></code>	Field name to sort by (default: "Reference").
<code>--sort-asc</code>	If given, sort in ascending order. If not given, sort in descending order.
<code>--filter <filter></code>	Filter string to remove output lines.
<code>--exclude-dnp</code>	Exclude symbols with the "Do not populate" attribute.
<code>--include-excluded-from-bom</code>	Include symbols marked "Exclude from BOM". This argument is deprecated as of KiCad 10.0 and has no effect.
<code>--field-delimiter <delimiter></code>	Separator between output fields/columns (default: ", ").
<code>--string-delimiter <delimiter></code>	Character to surround fields with (none by default).
<code>--ref-delimiter <delimiter></code>	Character to place between individual references (default: ", ").
<code>--ref-range-delimiter <delimiter></code>	Character to place in ranges of references (default: "-"). Leave blank for no ranges.
<code>--keep-tabs</code>	Keep tab characters from input fields. Stripped by default.
<code>--keep-line-breaks</code>	Keep line break characters from input fields. Stripped by default.

Schematic DXF export

The `sch export dxf` command exports a schematic to a DXF file. Each sheet in the design is exported to its own file.

Usage: `kicad-cli sch export dxf [--help] [--output OUTPUT_DIR] [--drawing-sheet SHEET_PATH] [--define-var KEY=VALUE]... [--variant VAR]... [--theme THEME_NAME] [--black-and-white] [--exclude-drawing-sheet] [--default-font VAR] [--draw-hop-over] [--pages PAGE_LIST] INPUT_FILE`

Positionsargumente:

<code>INPUT_FILE</code>	Schematic file to export.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the DXF file export command.
<code>-o <output dir>, --output <output dir></code>	The output folder for the exported files. One file is output for each sheet. When <code>--output</code> is not used, the files are exported to the current directory.
<code>--drawing-sheet <sheet path></code>	Path to drawing sheet to use in plot, overriding the drawing sheet specified in the schematic file.
<code>-D <variable name>=<value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>#{VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.
<code>-t <theme name>, --theme <theme name></code>	The name of the theme to use for export. If no theme is given, the schematic editor's currently selected theme is used.
<code>-b, --black-and-white</code>	Export schematic in black and white.
<code>-e, --exclude-drawing-sheet</code>	Plot DXF without a drawing sheet.
<code>--default-font </code>	Default font name. Default: "KiCad Font".
<code>--draw-hop-over</code>	Draw hop-overs at wire crossings.
<code>--pages <page list></code>	Comma-separated list of pages to export. Blank or unspecified means all pages. To plot specific pages, give the root sheet as <code>INPUT_FILE</code> and specify the desired output pages with the <code>--pages</code> argument.

Schematic HPGL export

NOTE

`kicad-cli sch export hpgl` is not functional in KiCad 10.0.

The `sch export hpgl` command is not functional in KiCad 10.0 as KiCad no longer supports HPGL output. In previous versions of KiCad it exported a schematic to an HPGL file. It is included as a non-functional command for compatibility reasons. It will be removed in a future version of KiCad.

Usage: `kicad-cli sch export hpgl [--help] [--output OUTPUT_DIR] [--drawing-sheet SHEET_PATH] [--define-var KEY=VALUE]... [--variant VAR]... [--theme THEME_NAME] [--black-and-white] [--exclude-drawing-sheet] [--default-font VAR] [--draw-hop-over] [--pages PAGE_LIST] [--pen-size PEN_SIZE] [--origin ORIGIN] INPUT_FILE`

Schematic netlist export

The `sch export netlist` command exports a netlist in [various formats](#) from a schematic.

Usage: `kicad-cli sch export netlist [--help] [--output OUTPUT_FILE] [--variant VAR]... [--format FORMAT] INPUT_FILE`

Position arguments:

<code>INPUT_FILE</code>	Schematic file to export.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the netlist export command.
<code>-o <output filename>, --output <output filename></code>	The output filename. When <code>--output</code> is not used, the output filename will be the same as the input file, with a <code>.net</code> file extension.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>#{VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.
<code>--format <format></code>	The netlist output format. Options are <code>kicadsexpr</code> (default), <code>kicadxml</code> , <code>cadstar</code> , <code>orcadpcb2</code> , <code>spice</code> , <code>spicemodel</code> , <code>pads</code> , or <code>allegro</code> .

Schematic PDF export

The `sch export pdf` command exports a schematic to a PDF file. Each sheet in the design is exported to its own page in the PDF file.

Usage: `kicad-cli sch export pdf [--help] [--output OUTPUT_FILE] [--drawing-sheet SHEET_PATH] [--define-var KEY=VALUE]... [--variant VAR]... [--theme THEME_NAME] [--black-and-white] [--exclude-drawing-sheet] [--default-font VAR] [--draw-hop-over] [--exclude-pdf-property-popups] [--exclude-pdf-hierarchical-links] [--exclude-pdf-metadata] [--no-background-color] [--pages PAGE_LIST] INPUT_FILE`

Position arguments:

INPUT_FILE	Schematic file to export.
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Optional Arguments:

-h, --help	Show help for the PDF file export command.
-o <output filename>, --output <output filename>	The output filename. When --output is not used, the output filename will be the same as the input file, with a .pdf file extension.
--drawing-sheet <sheet path>	Path to drawing sheet to use in plot, overriding the drawing sheet specified in the schematic file.
-D <variable name>=<value>, --define-var <variable_name>=<value>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
--variant <variant name>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use \${VARIANT} in the output path to generate separate files for each variant. When --variant is not used, the default variant is output.
-t <theme name>, --theme <theme name>	The name of the theme to use for export. If no theme is given, the schematic editor's currently selected theme is used.
-b, --black-and-white	Export schematic in black and white.
-e, --exclude-drawing-sheet	Plot PDF without a drawing sheet.
--default-font 	Default font name. Default: "KiCad Font".
--draw-hop-over	Draw hop-overs at wire crossings.
--exclude-pdf-property-popups	Do not generate property popups in PDF.
--exclude-pdf-hierarchical-links	Do not generate clickable links for hierarchical elements in PDF.
--exclude-pdf-metadata	Do not generate PDF metadata from AUTHOR and SUBJECT variables.
-n, --no-background-color	Export schematic without a background color, regardless of theme.
--pages <page list>	Comma-separated list of pages to export. Blank or unspecified means all pages. To plot specific pages, give the root sheet as INPUT_FILE and specify the desired output pages with the --pages argument.

Schematic PostScript export

The `sch export ps` command exports a schematic to a PostScript file. Each sheet in the design is exported to its own file.

Usage: `kicad-cli sch export ps [--help] [--output OUTPUT_DIR] [--drawing-sheet SHEET_PATH] [--define-var KEY=VALUE]... [--variant VAR]... [--theme THEME_NAME] [--black-and-white] [--exclude-drawing-sheet] [--default-font VAR] [--draw-hop-over] [--no-background-color] [--pages PAGE_LIST] INPUT_FILE`

Position arguments:

<code>INPUT_FILE</code>	Schematic file to export.
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Optional Arguments:

<code>-h, --help</code>	Show help for the PS file export command.
<code>-o <output dir>, --output <output dir></code>	The output folder for the exported files. One file is output for each sheet. When <code>--output</code> is not used, the files are exported to the current directory.
<code>--drawing-sheet <sheet path></code>	Path to drawing sheet to use in plot, overriding the drawing sheet specified in the schematic file.
<code>-D <variable name>=<value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>#{VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.
<code>-t <theme name>, --theme <theme name></code>	The name of the theme to use for export. If no theme is given, the schematic editor's currently selected theme is used.
<code>-b, --black-and-white</code>	Export schematic in black and white.
<code>-e, --exclude-drawing-sheet</code>	Plot PS without a drawing sheet.
<code>--default-font </code>	Default font name. Default: "KiCad Font".
<code>--draw-hop-over</code>	Draw hop-overs at wire crossings.
<code>-n, --no-background-color</code>	Export schematic without a background color, regardless of theme.
<code>--pages <page list></code>	Comma-separated list of pages to export. Blank or unspecified means all pages. To plot specific pages, give the root sheet as <code>INPUT_FILE</code> and specify the desired output pages with the <code>--pages</code> argument.

Schematic bill of materials export (legacy BOM scripts)

The `sch export python-bom` command exports an XML BOM file from a schematic. The XML BOM file can then be processed into your desired BOM format using a custom script or one of the scripts described in the [schematic BOM export documentation](#).

Usage: `kiCad-cli sch export python-bom [--help] [--output OUTPUT_FILE] INPUT_FILE`

Position arguments:

<code>INPUT_FILE</code>	Schematic file to export.
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Optional Arguments:

<code>-h, --help</code>	Show help for the BOM export command.
<code>-o <output filename>, --output <output filename></code>	The output filename. When <code>--output</code> is not used, the output filename will be the same as the input file, with a <code>-bom.xml</code> suffix and file extension.

Schematic SVG export

The `sch export svg` command exports a schematic to an SVG file. Each sheet in the design is exported to its own file.

Usage: `kiCad-cli sch export svg [--help] [--output OUTPUT_DIR] [--drawing-sheet SHEET_PATH] [--define-var KEY=VALUE]... [--variant VAR]... [--theme THEME_NAME] [--black-and-white] [--exclude-drawing-sheet] [--default-font VAR] [--draw-hop-over] [--no-background-color] [--pages PAGE_LIST] INPUT_FILE`

Position arguments:

<code>INPUT_FILE</code>	Schematic file to export.
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Optional Arguments:

<code>-h, --help</code>	Show help for the SVG file export command.
<code>-o <output dir>, --output <output dir></code>	The output folder for the exported files. When <code>--output</code> is not used, the files are exported to the current directory.
<code>--drawing-sheet <sheet path></code>	Path to drawing sheet to use in plot, overriding the drawing sheet specified in the schematic file.
<code>-D <variable name>=<value>, --define-var <variable_name>=<value></code>	Add or override project variable definitions. Can be used multiple times to define multiple variables.
<code>--variant <variant name></code>	The name(s) of the variant(s) to output. Can be used multiple times to output multiple variants. When specifying multiple variants, use <code>#{VARIANT}</code> in the output path to generate separate files for each variant. When <code>--variant</code> is not used, the default variant is output.
<code>-t <theme name>, --theme <theme name></code>	The name of the theme to use for export. If no theme is given, the schematic editor's currently selected theme is used.
<code>-b, --black-and-white</code>	Export schematic in black and white.
<code>-e, --exclude-drawing-sheet</code>	Plot SVG without a drawing sheet.
<code>--default-font </code>	Default font name. Default: "KiCad Font".
<code>--draw-hop-over</code>	Draw hop-overs at wire crossings.
<code>-n, --no-background-color</code>	Export schematic without a background color, regardless of theme.
<code>--pages <page list></code>	Comma-separated list of pages to export. Blank or unspecified means all pages. To plot specific pages, give the root sheet as <code>INPUT_FILE</code> and specify the desired output pages with the <code>--pages</code> argument.

Schematic upgrade

The `sch upgrade` command converts a KiCad schematic file from a previous KiCad schematic file format to the native format for the current version of KiCad. If the input schematic file is already in the current file format, no action is taken.

NOTE

Only the specified schematic file is upgraded. If the schematic file contains any child sheets, the child sheets are not upgraded.

Usage: `kiCAD-cli sch upgrade [--help] [--force] INPUT_FILE`

Position arguments:

<code>INPUT_FILE</code>	Schematic file to upgrade.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the upgrade command.
<code>--force</code>	Re-save the input schematic file even if it is already in the current file format.

Symbol commands

The `sym` subcommand exports symbols to another format or upgrades symbol libraries to the current version of the KiCad symbol file format.

Symbol export

The `sym export svg` command exports one or more symbols from the specified library into SVG files.

Usage: `kicad-cli sym export svg [--help] [--output OUTPUT_DIR] [--theme THEME_NAME] [--symbol SYMBOL] [--black-and-white] [--include-hidden-pins] [--include-hidden-fields] INPUT_FILE`

Position arguments:

<code>INPUT_FILE</code>	Symbol library file to use for export.
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Optionale Argumente:

<code>-h, --help</code>	Show help for the symbol SVG export command.
<code>-o <output dir>, --output <output dir></code>	The output folder for the exported files. Each symbol in the input library is output to a separate file. When <code>--output</code> is not used, the files are exported to the current directory.
<code>-t <theme name>, --theme <theme name></code>	The name of the theme to use for export. If no theme is given, the symbol editor's currently selected theme is used.
<code>-s <symbol name>, --symbol <symbol name></code>	The specific symbol to export from the library. When this argument is not used, all symbols in the library are exported.
<code>--black-and-white</code>	Export symbols in black and white.
<code>--include-hidden-pins</code>	Export hidden pins in the exported SVG.
<code>--include-hidden-fields</code>	Export hidden symbol fields in the exported SVG.

Symbol upgrade

The `sym upgrade` command converts the specified symbol library from a legacy KiCad symbol format or a non-KiCad symbol format to the native format for the current version of KiCad. If the input library is already in the current file format, no action is taken.

Supported input symbol formats are:

- KiCad symbol library (`.kicad_sym`)
- KiCad (pre-6.0) symbol library (`.lib`)
- Altium schematic library (`.SchLib`)
- Altium integrierte Bibliothek (`.IntLib`)
- CADSTAR parts library (`.lib`)

- EAGLE XML-Bibliothek (`.lbr`)
- EasyEDA (JLCEDA) Std-Datei (`.json`)
- EasyEDA (JLCEDA) Pro-Datei (`.elibz` , `.epro` , `.zip`)

Usage: `kicad-cli sym upgrade [--help] [--output OUTPUT_FILE_OR_DIR] [--force] INPUT_FILE_OR_DIR`

Positionsargumente:

<code>INPUT_FILE_OR_DIR</code>	Symbol or symbol library to upgrade. This can be an unpacked symbol (<code>.kicad_sym</code> file containing a single symbol), an unpacked symbol library (folder containing <code>.kicad_sym</code> files), or a packed symbol library (<code>.kicad_sym</code> file containing multiple symbols).
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Optionale Argumente:

<code>-h, --help</code>	Show help for the upgrade command.
<code>-o <output file or directory>, --output <output file or directory></code>	The output file or directory for the upgraded symbol library. When the output path is a file, the symbols are saved as a single-file ("packed") <code>.kicad_sym</code> library. When the output path is a folder, the symbols are saved as individual ("unpacked") <code>.kicad_sym</code> files in the folder, with one file per symbol. When <code>--output</code> is not used, the upgraded symbol library is saved over the original library.
<code>--force</code>	Re-save the input library even if it is already in the current file format.

Version commands

The `version` command prints the KiCad version. Without any arguments, it simply prints the version number, for example `7.0.7`. You can print the version in several other formats using the `--format` argument.

NOTE

Use `kicad-cli version --format about` for version information to include when submitting bug reports or feature requests on Gitlab.

Usage: `kicad-cli version [--help] [--format VAR]`

Optionale Argumente:

<code>-h, --help</code>	Show help for the version command.
<code>--format <format></code>	Format of the version number. Options are <code>plain</code> (default), <code>commit</code> , or <code>about</code> . <code>plain</code> prints the version number (e.g. <code>7.0.7</code>), which is the default if the <code>--format</code> argument is not used. <code>commit</code> prints the hash of the git commit for the build of KiCad you are using. <code>about</code> prints the full version information, including library versions and basic system information. You can use the <code>about</code> version information in bug reports.